

**<Note>**

\* CPU: If you equip single CPU, it has to be equipped into the socket of **CPU0** (which is near by edge of bracket)

**\*Memory Allocation**

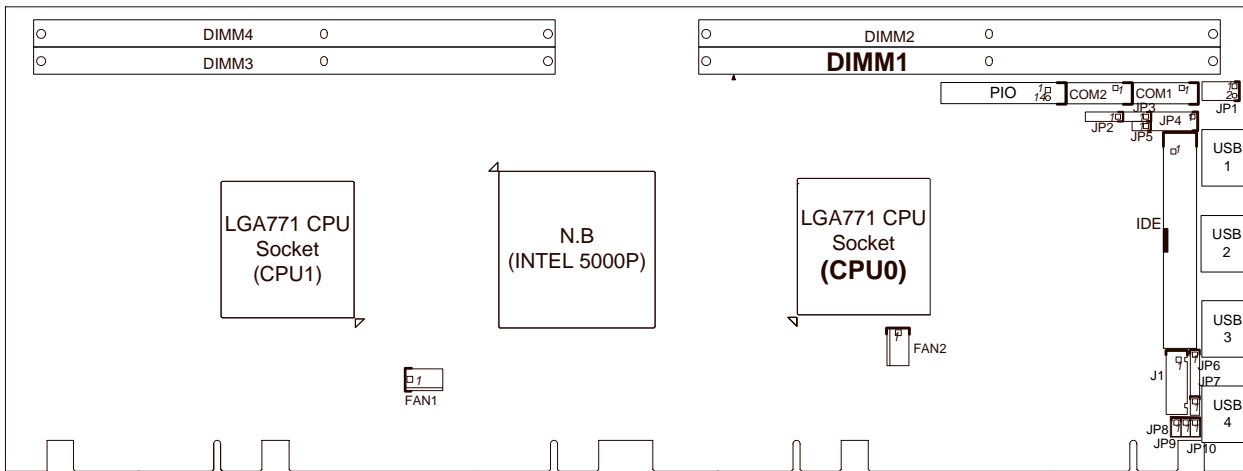
- A) When it's Single Memory => **DIMM 1**
- B) When they're Two Memories => DIMM 1+ DIMM 2
- C) Four Memories => DIMM 1 + DIMM 2  
DIMM 3 + DIMM 4

\*Quick reference guide is subject to change without prior notice. For any update, please visit our website:

[www.nexcom.com](http://www.nexcom.com)

\* [\* ]=Default

## CPU Board



**JP1:LAN Link/Active**

|     |              |     |            |     |              |     |            |
|-----|--------------|-----|------------|-----|--------------|-----|------------|
| Pin | Def.         | Pin | Def.       | Pin | Def.         | Pin | Def.       |
| 7   | 3.3VSB       | 5   | 3.3VSB     | 3   | 3.3VSB       | 1   | 3.3VSB     |
| 8   | LAN2_ACTLED# | 6   | LAN2_LINK# | 4   | LAN1_ACTLED# | 2   | LAN1_LINK# |

**JP2:Speaker (Reserved)**

|     |            |     |      |     |      |     |      |
|-----|------------|-----|------|-----|------|-----|------|
| Pin | Def.       | Pin | Def. | Pin | Def. | Pin | Def. |
| 4   | IO_SPEAKER | 3   | GND  | 2   | GND  | 1   | +5V  |

**JP3:CMOS Clear**

|     |            |     |             |     |        |
|-----|------------|-----|-------------|-----|--------|
| Pin | Def.       | Pin | Def.        | Pin | Def.   |
| 3   | CMOS_CLEAR | 2   | ESB_RTCRST# | 1   | RTCST# |

**JP4:GPIO**

|     |       |     |       |     |       |     |       |     |      |
|-----|-------|-----|-------|-----|-------|-----|-------|-----|------|
| Pin | Def.  | Pin | Def.  | Pin | Def.  | Pin | Def.  | Pin | Def. |
| 9   | GPO17 | 7   | GPO16 | 5   | GPO15 | 3   | GPO14 | 1   | +5V  |
| 10  | GPI13 | 8   | GPI12 | 6   | GPI11 | 4   | GPI10 | 2   | GND  |

**JP5:DOM**

|     |      |     |      |
|-----|------|-----|------|
| Pin | Def. | Pin | Def. |
| 2   | GND  | 1   | +5V  |

FAN1:CPU1 FAN  
FAN2:CPU0 FAN

**J1:USB port 4/5**

|     |           |
|-----|-----------|
| Pin | Def.      |
| 1   | USB_VCC45 |
| 2   | USB_4N    |
| 3   | USB_4P    |
| 4   | USB_5N    |
| 5   | USB_5P    |
| 6   | USB_GND   |

**JP6:Key lock**

|     |         |
|-----|---------|
| Pin | Def.    |
| 1   | +5V     |
| 2   | NC      |
| 3   | GND     |
| 4   | KBLOCK# |
| 5   | GND     |

**JP7 :Main power LED**

|     |      |
|-----|------|
| Pin | Def. |
| 1   | +5V  |
| 2   | GND  |

**JP8:HW Reset**

|     |         |
|-----|---------|
| Pin | Def.    |
| 1   | HW_RST# |
| 2   | GND     |

**JP9:Push button**

|     |          |
|-----|----------|
| Pin | Def.     |
| 1   | SIO_PBIN |
| 2   | GND      |

**JP10:HD LED**

|     |        |
|-----|--------|
| Pin | Def.   |
| 1   | +5V    |
| 2   | HD_LED |

## IO Board

